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Re:

U.S. Patent Application of Rafael Reif et al.

Application No.: 10/749,103

Entitled: METHOD OF FORMING A MULTI-LAYER SEMICONDUCTOR STRUCTURE

INCORPORATING A PROCESSING HANDLE MEMBER

Filed On: December 30, 2003

MIT Case No.: 10003

DCM Case No.: MIT-136BUS

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PAGE 02/17

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TRANSMITTAL FORM  (to be used for all correspondence after initial filing)		Application Number Filing Date First Named Inventor	10/749,103 December 30, 2003 Rafael Reif			
		Art Unit Examiner Name	2823 Not Yet Assigned			
Total Number of Pages in This Submiss	Total Number of Pages in This Submission 16		MIT-136BUS			
ENCLOSURES (Check all that apply)						
Fee Transmittal Form Fee Attached  Amendment/Reply After Final Affidavits/declaration(s)  Extension of Time Request Express Abandonment Request Information Disclosure Statem Certified Copy of Priority Document(s)  Response to Missing Parts/ Incomplete Application  Response to Missing I under 37 CFR 1.52 or	Parts In the eprovider charge	vent a petition for extension of d, such petition is hereby made deposit account No. 50-0845 f	Return Postcard PTO SB/08A with Four (4) References Cited  time is required by this paper and not otherwise and authorization is provided herewith to or the cost of such extension.			
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This collection of Information is required by 37 CFR 1.5. The Information is required to obtain or retain a bornefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to 2 hours to complete, including gathering, preparing, and automitting the completed application form to the USPTO. Time will vory depending upon the Individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Petant and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450, DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS, SEND TO: Mail Stop Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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PATENT

### IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Appl. No.

10/749,103

Confirmation No.: 3165

Applicant Filed Rafael Reif et al.
December 30, 2003

T.C./A.U.

: 2823

Examiner

Not Yet Assigned

Docket No.

MIT-136BUS

Customer No.

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Dear Sir:

It is desired to cite for the record in this application the enclosed documents listed on the attached copy of PTO form PTO/SB/08A (Formerly #1449). The paragraph(s) marked below are applicable to this Information Disclosure Statement.

[X] (1) The enclosed Information Disclosure Statement is being filed: within three months of the filing date; or within three months of the entry of the national stage of the above-identified application; or before the mailing of a first Office Action on the merits; or before the mailing of a first Office Action after the filing of a request for continued examination under 37 C.F.R. §1.114. Accordingly, Applicant(s) believes that no fee or statement is required.

Docket No. MIT-136BUS

[ ] (2) Pursuant to 37 C.F.R. § 1.97(c), the enclosed Information Disclosure Statement is being filed before the mailing date of a final action or a notice of allowance and is accompanied by the fee set forth in 37 C.F.R. § 1.17(p).

# SUBMISSION UNDER 37 C.F.R. § 1.97(c) INCLUDING A STATEMENT UNDER 37 C.F.R. § 1.97(e)(1)

[ ] (3) Pursuant to 37 C.F.R. § 1.97(c), the enclosed Information Disclosure Statement is being filed before the mailing date of a final action or a notice of allowance and is accompanied by a statement under 37 C.F.R. § 1.97(e)(1). The undersigned hereby states that each item of information contained in the attached Information Disclosure Statement was first cited in any communication from a foreign patent office in a counterpart foreign application mailed not more than three months prior to the filing of the accompanying Information Disclosure Statement.

# SUBMISSION UNDER 37 C.F.R. § 1.97(c) INCLUDING A STATEMENT UNDER 37 C.F.R. § 1. 97(e)(2)

[ ] (4) Pursuant to 37 C.F.R. § 1.97(c), the enclosed Information Disclosure Statement is being filed before the mailing date of a final action or a notice of allowance and is accompanied by a statement under 37 C.F.R. § 1.97(e)(2). The undersigned hereby states that no item of information contained in the accompanying Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application, and, to the knowledge of the undersigned, after making reasonable inquiry, no item of information contained in the accompanying Information Disclosure Statement was known to any individual having a duty of disclosure as set forth in 37 C.F.R. § 1.56 (c) more than three months prior to the filing of the accompanying Information Disclosure Statement.

Docket No. MIT-136BUS

#### SUBMISSION UNDER 37 C.F.R. § 1.97(c)

### INCLUDING STATEMENTS UNDER 37 C.F.R. §§ 1.704(d) AND 1.97(e)(1)

[ ] (5) Pursuant to 37 C.F.R. § 1.97(c), the enclosed Information Disclosure Statement is being filed before the mailing date of a final action or a notice of allowance and is accompanied by statements under 37 C.F.R. §§ 1.704(d) and 1.97(e)(1). The undersigned hereby states that each item of information contained in the attached Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application mailed not more than three months prior to the filing of the accompanying Information Disclosure Statement and was not received by any individual designated in §1.56(c) more than thirty days prior to the filing of the accompanying Information Disclosure Statement.

# SUBMISSION UNDER 37 C.F.R. § 1.97(d) INCLUDING A STATEMENT UNDER 37 C.F.R. §1.97(e)(1)

[ ] (6) Pursuant to 37 C.F.R. § 1.97(d), the enclosed Information Disclosure Statement is being filed on or before payment of the issue fee and is accompanied by a statement under 37 C.F.R. § 1.97(e)(1) and the fee required under 37 C.F.R. § 1.17(p). The undersigned hereby states that each item of information contained in the accompanying Information Disclosure Statement was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of the Information Disclosure Statement.

# SUBMISSION UNDER 37 C.F.R. § 1.97(d) INCLUDING A STATEMENT UNDER 37 C.F.R. §1.97(e)(2)

[ ] (7) Pursuant to 37 C.F.R. § 1.97(d), the enclosed Information Disclosure Statement is being filed on or before payment of the issue fee and is accompanied by a statement under 37 C.F.R. § 1.97(e)(2) and the fee required under 37 C.F.R. § 1.17(p). The undersigned hereby states that no item of information contained in the accompanying Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application, and, to the knowledge of the undersigned, after making reasonable inquiry, no item of information contained in the accompanying Information Disclosure Statement was known to

Docket No. MIT-136BUS

any individual having a duty of disclosure as set forth in 37 C.F.R. § 1.56(c) more than three months prior to the filing of the accompanying Information Disclosure Statement.

### SUBMISSION UNDER 37 C.F.R. § 1.97(d)

#### INCLUDING STATEMENTS UNDER 37 C.F.R. §§ 1.704(d) AND 1.97(e)(1)

[ ] (8) Pursuant to 37 C.F.R. § 1.97(d), the enclosed Information Disclosure Statement is being filed on or before payment of the issue fee and is accompanied by statements under 37 C.F.R. §§ 1.704(d) and 1.97(e)(1) and the fee required under 37 C.F.R. § 1.17(p). The undersigned hereby states that each item of information contained in the attached Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application mailed not more than three months prior to the filing of the accompanying Information Disclosure Statement and was not received by any individual designated in §1.56(c) more than thirty days prior to the filing of the accompanying Information Disclosure Statement.

The filing of this Information Disclosure Statement is not a representation by the undersigned as to personal knowledge of the contents of every word or phrase of the material enclosed or that reliance on other suitably trained professionals has not been made.

If a search report of a searching agency is enclosed identifying the nature of the relevance of each document, such a designation is deemed to satisfy Rule 98(a) (3) even if in a foreign language, since the few terms of relevance therein are deemed of universal cognizance. However, Applicant(s) does not necessarily adopt the position reflected by that report.

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The Commissioner is hereby authorized to charge payment of any additional fees associated with this communication or credit any overpayment to Deposit Account No. 500845.

Dated: 29JUN04

Respectfully submitted,

Daly, Crowley & Mofford, <del>LL</del>I

By: Christopher 8. Daly

Reg. No. 37,303

Attorney for Applicant(s) 275 Tumpike Street, Suite 101 Canton, MA 02021-2354

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300300	(8 10 10 tt) (T-0) C			Application Number	10/749,103
INFORMATION DISCLOSURE				Filing Date	December 30, 2003
(Use as many shoots as necessary)			PPLICANT	First Named Inventor	Rafael Reif
				Art Unit	2823
			ecossary)	Examiner Name	Not Yet Assigned
Sheet	1	of	1	Attorney Docket Number	MIT-136BUS

Examiner Initials*	Cite No.1	OTHER PRIOR ART-NON PATENT LITERATURE DOCUMENTS  Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
		CHRIS GAITHER, Intel Set To Unvail Advanced Communications Chip, The Boston Globe, October 15, 2002, Pages 1-3, Boston, Massachusetts	
		PETER CLARKE, IEDM Late Paper: IBM Shows How to Stack for 3D Circuitry, Silicon Strategies.Com, November 28, 2002, Pages 1-2, LONDON	
		TRU-SI TECHNOLOGIES, Through-Siticon Vias, Tru-Si Technologies Home Page, April 7, 2003, Page 1, Sunnyvale, California	
		MARK LAPEDUS, Tezzaron Devises Multi-Wafer Stack for 3-D Chips, Semiconductors EE Times, June 30, 2003, Pages 1-3, Naperville, Illinois	
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